

HPR12

HNR12

High-Performance Voltage Regulators

For detailed operation and installation instructions and specifications, please download the datasheet for this product from our website.



Features

- Ultra-low noise
- Wide bandwidth
- Excellent ripple rejection
- Low, load-independent output impedance
- Kelvin sense connections
- Size and pin compatible with TO-220 style regulators

Applications

- Preamplifiers and buffers
- RIAA input stages
- Playback head amplifiers
- Performance upgrade for existing products

Description

The HPR12/HNR12 are high performance voltage regulators targeted at performance-critical audio applications. Key performance parameters exceed that of industry-standard integrated circuits by several orders of magnitude. Kelvin sense connections provide point-of-load (local) regulation without requiring physical proximity.

General Performance Data

VI=18V, IO=10mA, Tamb =25°C, unless otherwise noted

Parameter	Conditions	Symbol	Min	Тур	Max	Unit	Note
Input Voltage		VIN	15	18	25	V	1)
Thermal dissipation		TDISP		0,5	0,75	W	1) 2)
Output Voltage		VOUT	11.5	12	12.5	V	
Output Impedance	Tj =25°C	ZOUT	-	50	70	mΩ	
Ripple Rejection	Tj =25°C	PSRR	105	110	-	dB	
Output Noise Voltage	MBW=10Hz to 20kHz	VN	-	290	400	nV	
Dropout Voltage		VD	-	-	3	V	
Quiescent Current	Tj =25°C	IQ	-	14	20	mA	
Output Current Range		Іоит	0	-	100	mA	
Short-Circuit Current		Isc	-	500	-	mA	
Optimum Load Capaci- tance Range		CL	2.2	-	~~~~	μF	



Attention: Observe precautions for handling electrostatic sensitive devices. This module uses semiconductors that can be damaged by electrostatic discharge (ESD). Damage due to inappropriate handling is not covered by warranty.



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Environmental Specifications

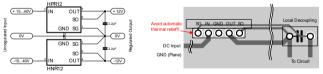
Parameter	Conditions	Symbol	Min	Тур	Max	Unit	Not e
Ambient Temperature	Storage		-25	-	70	°C	
	Operation	Tamb	0	-	50	°C	
Thermal resistance		Reja		105		°C/W	
Humidity	Max 85 percent relative humidity, non-condensing.						

Connections

The middle three pins on the HxR12 modules match the pin-out of the standard LM7812CT/LM7912CT devices. If the decoupling capacitor is placed very close to the regulator, full pin-compatibility can be achieved by shorting the two solder jumpers on the back of the module and clipping the two outer pins. This adversely affects all performance parameters so this should only be done when retrofitting pre-existing equipment. In case of doubt, run two wires from the decoupling capacitor to the Kelvin sense connections.

	HPR12		HNR12		Top View (HPR) SG IN GND OUT SO
Pin	Name	Function	Name	Function	00000
1	SG	Ground Sense input	SG	Ground Sense input	1 2 3 4 5
2	IN	Unregulated input	GND	Ground connection	
3	GND	Ground connection	IN	Unregulated input	Top View (HNR) SG GND IN OUT SO
4	OUT	Regulated Output	OUT	Regulated Output	00000
5	SO	Output Sense input	SO	Output Sense input	1 2 3 4 5

Typical Application Schematic



PCB Layout Considerations

The HxR12 regulators are fast, high-performance circuits. Care must be taken not to degrade performance by lay-out oversights. Most CAD tools automatically assign the output and sense pins to the same net. The sense traces must be routed manually from the decoupling capacitor to the regulator. When using a ground plane, special care should be taken to keep the layout program from drawing a thermal relief star straight to the ground plane. If necessary, use "0 ohm" jumpers to forcibly separate the negative kelvin serve from the GND net. Sense traces should be treated like a differential signal connection. Run them parallel and close to each other, not necessarily parallel to the output traces. The regulator has minimal decoupling internally. If the power traces from the unregulated supply to the regulator met particularly long, some extra decoupling in the form of a series resistor and a smal electrolytic capacitor might be added.